



**ER4953A**

**-30V Dual P-Channel MOSFET**

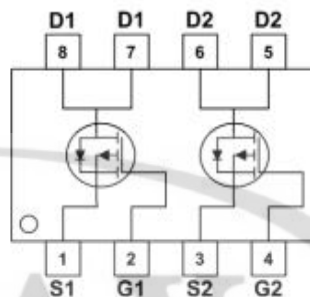
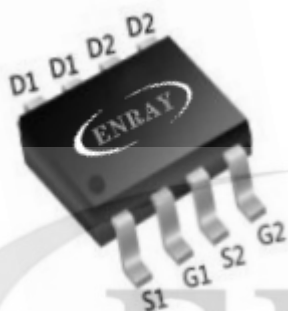
**Features**

The ER4953A is the high cell density trench P-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The ER4953A meet the RoHS and Green Product.

**Product Summary**

V <sub>DS</sub>	-30V
I <sub>D</sub>	-5.3A
R <sub>DS(ON)</sub> (at V <sub>GS</sub> =-10V)	<60mΩ
R <sub>DS(ON)</sub> (at V <sub>GS</sub> =-4.5V)	< 90mΩ

**Dual SOP-8**



**Maximum Ratings(T<sub>a</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>GS</sub>	±20	
Continuous Drain Current V <sub>GS</sub> @ 10V <sup>1</sup>	I <sub>D@TA=25°C</sub>	-5.3	A
Continuous Drain Current V <sub>GS</sub> @ 10V <sup>1</sup>	I <sub>D@TA=70°C</sub>	-4.3	
Pulsed Drain Current <sup>2</sup>	I <sub>DM</sub>	-20	
Single Pulse Avalanche Energy	E <sub>AS</sub>	-	mJ
Avalanche Current	I <sub>AS</sub>	-	
Total Power Dissipation	P <sub>D@TA=25°C</sub>	2.0	W
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to 150	

**Thermal Data**

Parameter	Symbol	Max.	Unit
Thermal Resistance Junction-ambient	R <sub>θJA</sub>	100	°C/W
Thermal Resistance Junction-Case	R <sub>θJC</sub>		°C/W

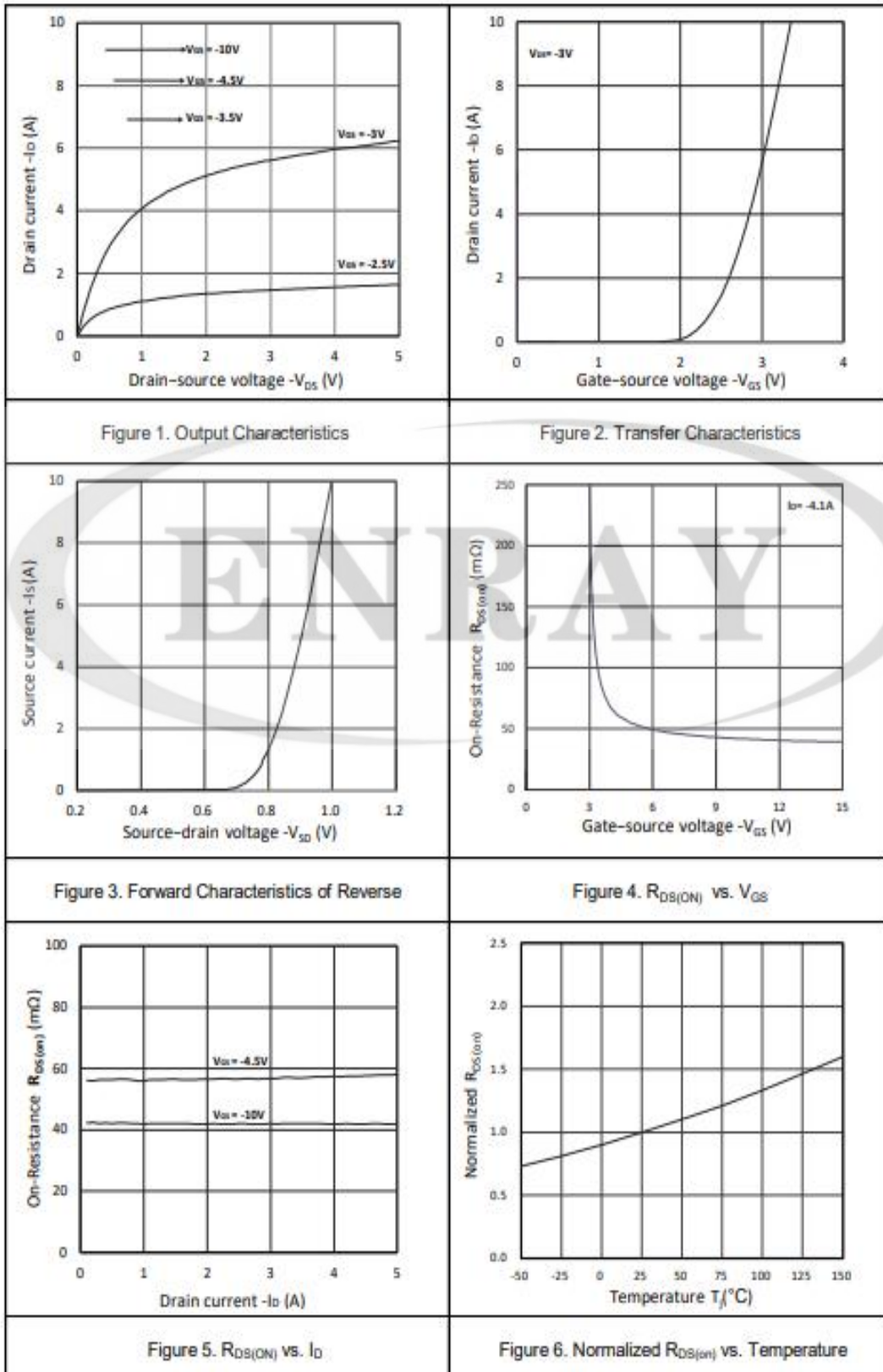


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**Electrical Characteristics(T<sub>J</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Parameters ③</b>						
Drain-Source Breakdown Voltage	BVDSS	VGS = 0V, ID = -250μA	-30			V
Gate Threshold Voltage	VGS(th)	VDS =VGS, ID = -250μA	-1.0	-1.5	-2.1	V
Gate-Body leakage Current	IGSS	VDS = 0V, VGS = ±20V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS = -30V, VGS = 0V			-1	μA
Static Drain-Source On-Resistance	RDS(on)	VGS = -10V, ID = -4.1A		35	60	mΩ
		VGS = -4.5V, ID = -3A		52	90	mΩ
<b>Dynamic Parameters ④</b>						
Input Capacitance	Ciss	VDS = -15V, VGS =0V, f =1MHz		530		pF
Output Capacitance	Coss			70		pF
Reverse Transfer Capacitance	Crss			56		pF
Total Gate Charge	Qg	VGS = -10V, ID = -4.1A, VDS = -15V		10		nC
Gate Source Charge	Qgs			2		nC
Gate Drain Charge	Qgd			2.8		nC
<b>Switching Parameters ④</b>						
Turn-On DelayTime	td(on)	VGS = -10V ,VDD= -15V, ,RGEN = 6Ω, ID = -4.1A,		6.9		ns
Turn-On Rise Time	tr			12		ns
Turn-Off DelayTime	td(off)			19		ns
Turn-Off Fall Time	tf			7.5		ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Continuous Source Current <sup>1,4</sup>	IS				-5.3	A
Diode Forward Voltage <sup>2</sup>	VSD	IS = -1.7A, VGS = 0V			-1.2	V
<b>Note :</b>						
1. Repetitive Rating : Pulse width limited by maximum junction temperature.						
2. Surface Mounted on FR4 Board, t < 5 sec.						
3. Pulse Test : Pulse Width≤300μs, Duty Cycle ≤ 2%.						
4. Guaranteed by design, not subject to production testing.						

Typical Electrical and Thermal Characteristics



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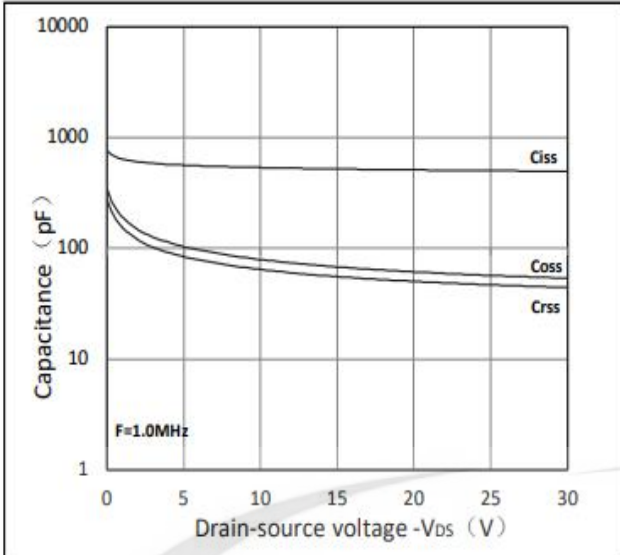


Figure 7. Capacitance Characteristics

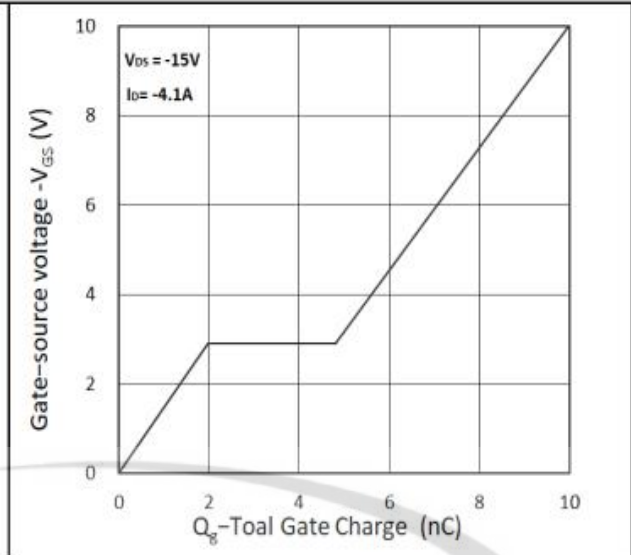
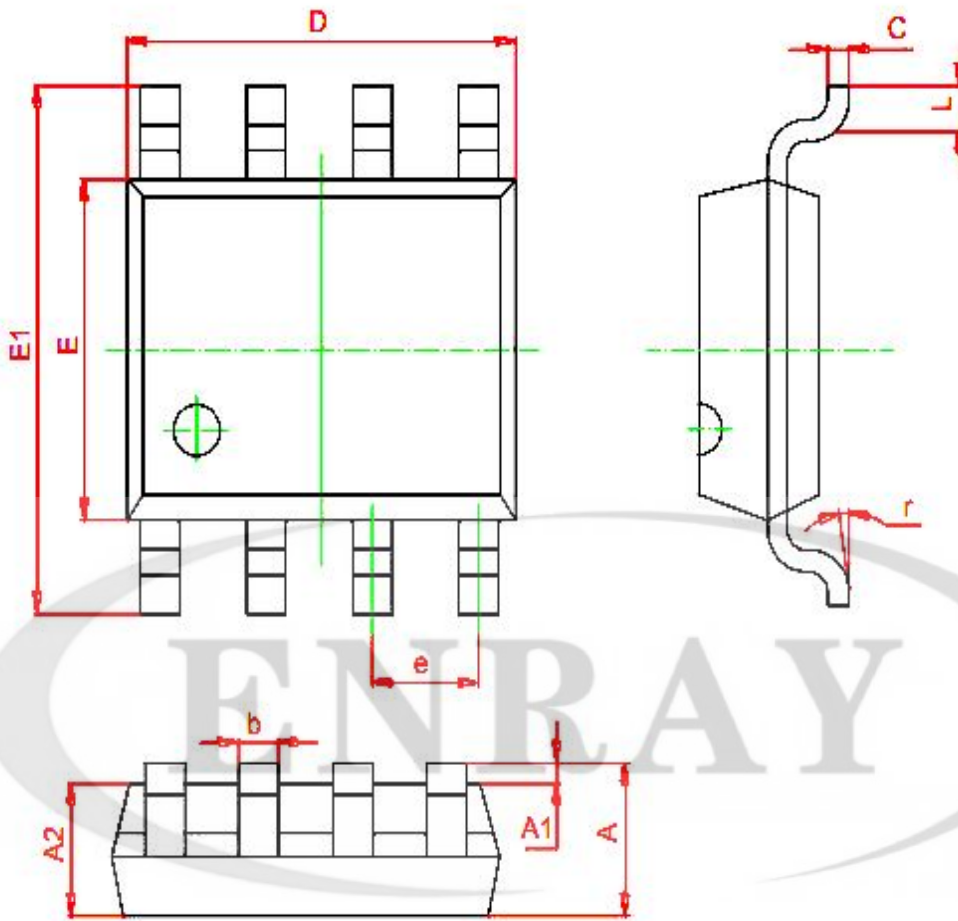


Figure 8. Gate Charge Characteristics

Dual SOP-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
C	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°